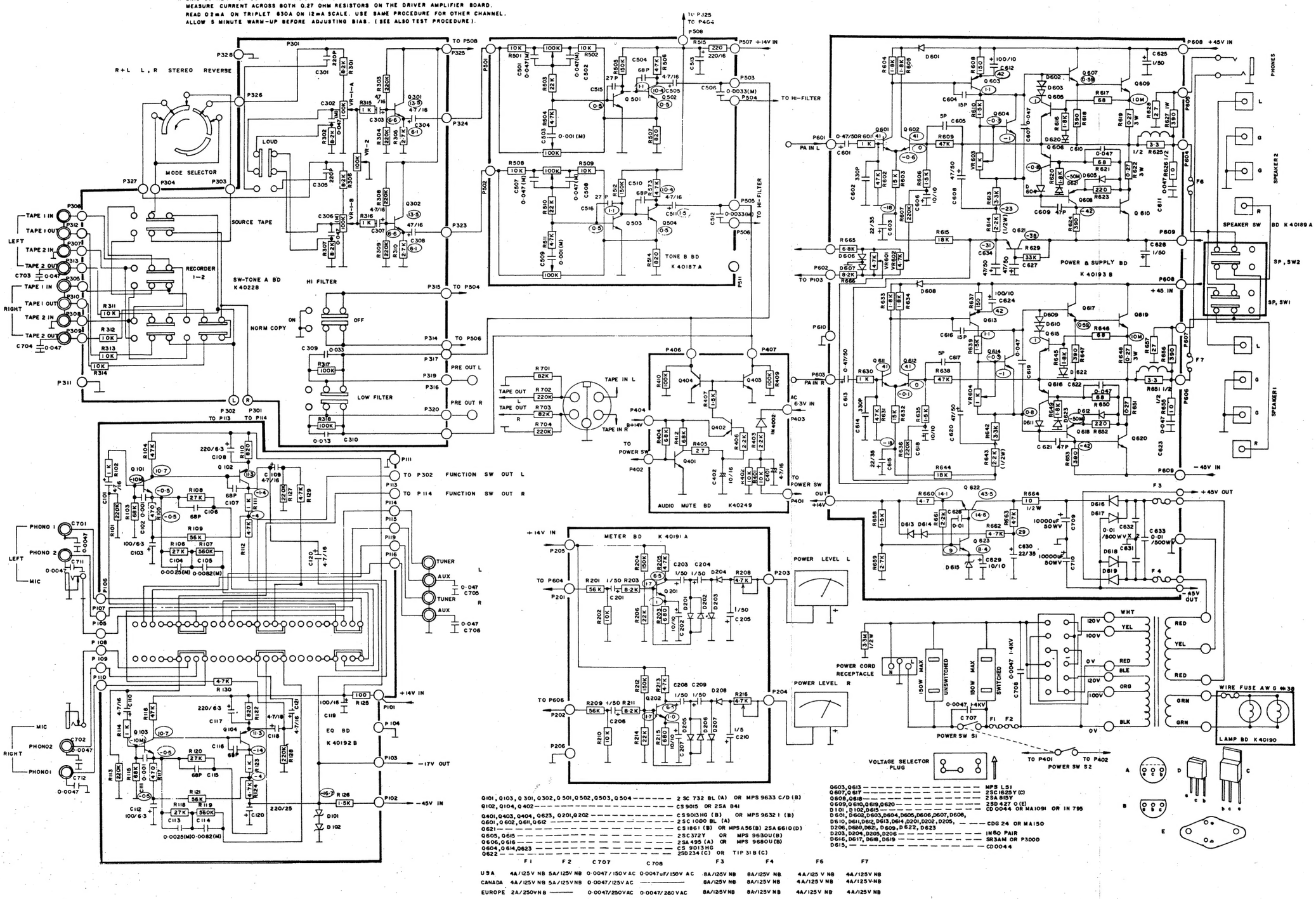
SCHEMATIC DIAGRAM
MODEL NO. A 457
SERIAL NO. 409401 & ABOVE.

NOTES.
1. RESISTANCE IN OHMS ±10%, 1/4W; CAPACITANCE IN MICROFARADS, UNLESS OTHERWISE SPECIFIED.
2. UNLESS OTHERWISE SPECIFIED, ALL DC VOLTAGES ±15% MEASURED WITH RESPECT TO CHASSIS GROUND, USING VTVM, POWER LINE 120VAC
3. DC OFFSET VOLTAGE - WITH NO SIGNAL APPLIED, READ 0 VOLTS ±25mV AT SPEAKER A OUTPUT TERMINAL (25 mV IS 1.25 mA ON TRIPLET 630A 12mA SCALE).
4. BIAS CURRENT OF OUTPUT TRANSISTORS IS MEASURED AS FOLLOWS. SET VOM TO 12mA RANGE. MEASURE CURRENT ACROSS BOTH 0.27 OHM RESISTORS ON THE DRIVER AMPLIFIER BOARD. READ 0.2mA ON TRIPLET 630A ON 12mA SCALE. USE SAME PROCEDURE FOR OTHER CHANNEL. ALLOW 5 MINUTE WARM-UP BEFORE ADJUSTING BIAS. (SEE ALSO TEST PROCEDURE).

SCOTT

SCHALTUNGSDIENST LANGE 1000 BERLIN 47 PF. 47 06 53
TEL. 030/603 20 03 TX 184339 BTX *360 310 000 140#



Q101, Q103, Q 301, Q302, Q 501, Q502, Q503, Q 504 ——— 2 SC 732 BL (A) OR MPS 9633 C/D (B)
Q102, Q104, Q 402 ——— CS 9015 OR 2SA 841
Q401, Q403, Q404, Q623, Q201, Q202 ——— CS 9013 HG (B) OR MPS 9632 I (B)
Q601, Q602, Q611, Q612 ——— 2 SC 1000 BL (A)
Q621 ——— CS 1861 (B) OR MPSA56(B) 2SA 6610 (D)
Q605, Q615 ——— 2SC 372Y OR MPS 9630U(B)
Q606, Q616 ——— 2SA 495 (A) OR MPS 9680U(B)
Q604, Q614, Q623 ——— CS 9013 HG
Q622 ——— 2SD 234 (C) OR TIP 31 B (C)

Q603, Q615 ——— MPS L51
Q607, Q617 ——— 2SC 1623 Y (C)
Q608, Q618 ——— 2SA 815 Y
Q609, Q610, Q619, Q620 ——— 2SD 427 O (E)
D101, D102, D615 ——— CD 0044 OR MA 1091 OR IN 795
D601, D602, D603, D604, D605, D606, D607, D608,
D610, D611, D612, D613, D614, D201, D202, D205, ——— CDG 24 OR MA 150
D206, D620, D621, D609, D622, D623
D203, D204, D205, D206 ——— IN 60 PAIR
D616, D617, D618, D619 ——— SR3AM OR P3000
D615, ——— CD 0044

	F1	F2	C707	C708	F3	F4	F6	F7
USA	4A/125V NB	5A/125V NB	0·0047/150VAC	0·0047uF/150V AC	8A/125V NB	8A/125V NB	4A/125 V NB	4A/125V NB
CANADA	4A/125V NB	5A/125VNB	0·0047/125VAC	———	8A/125V NB	8A/125V NB	4A/125 V NB	4A/125V NB
EUROPE	2A/250VNB	———	0·0047/250VAC	0·0047/280VAC	8A/125VNB	8A/125V NB	4A/125 V NB	4A/125V NB

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